

**Amendments to the Claims** are reflected in the listing of claims which begins on page 3 of this paper.

**Remarks/Arguments** begin on page 7 of this paper.

CLAIM AMENDMENTS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (previously presented). A circuit configuration, comprising:

an integrated power semiconductor element having a single diagnostic output, a logic element, a power switch for switching a load and having a load output, a current source, and a current sink; the load connected to said load output, the current source and the current sink being connected to said single diagnostic output and providing a current at said diagnostic output; said logic element controlling at least one of said power switch, and the current source and the current sink; and

a display element having light-emitting semiconductor diodes, a first terminal, and a second terminal; said first terminal electrically connected to said single diagnostic output of said integrated power semiconductor element; said second terminal electrically connected to said load output of said power switch; and said light-emitting semiconductor diodes being connected in antiparallel between said first terminal

and said second terminal and outputting different visual information items when receiving different currents.

Claim 2 (previously presented). The circuit configuration according to claim 1, wherein the current source and the current sink provide a current at said single diagnostic output having an intensity and flow direction, the intensity and flow direction providing an item of information regarding a status of said power switch.

Claim 3 (previously presented). The circuit configuration according to claim 2, wherein said power switch connects to the load.

Claim 4 (previously presented). The circuit configuration according to claim 2, wherein said integrated power semiconductor element connects to the load.

Claim 5 (previously presented). The circuit configuration according to claim 1, wherein the current source and current sink are disposed in a half-bridge configuration relative to one another having a center tap, and the center tap is connected to said single diagnostic output.

Claim 6 (previously presented). The circuit configuration according to claim 1, wherein the current source is a MOSFET.

Claim 7 (previously presented). The circuit configuration according to claim 1, wherein the current sink is a MOSFET.

Claim 8 (previously presented). The circuit configuration according to claim 1, wherein the current source and the current sink are MOSFETs.

Claim 9 (previously presented). The circuit configuration according to claim 1, wherein said integrated power semiconductor element further includes an input terminal, an output terminal connected to the load, and a supply terminal for receiving a supply potential.

Claim 10 (previously presented). The circuit configuration according to claim 9, wherein said integrated power semiconductor element is a low-side switch having a single supply terminal, at which a first supply potential is present, and outputting a second supply potential at said output terminal.

Claim 11 (previously presented). The circuit configuration according to claim 10, wherein at least one of the current

source and the current sink is connected directly and electrically to said input terminal.

Claim 12 (previously presented). The circuit according to claim 1, wherein said power switch is a power semiconductor component.

Claim 13 (original). The circuit according to claim 12, wherein said power semiconductor component is a power MOSFET.

Claims 14-19 (canceled)